

# Pranav Prakash

#SF3, Phase 3, Lotus Krest Apartment,  
Whitefield, Bangalore- 560066, India.

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## Education

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2004–2008	Bachelor of Technology (Honours) in Electrical Engineering from Indian Institute of Technology, Kharagpur with CGPA of 7.69/10.
2001–2003	Higher secondary school from Science College, Patna with aggregate of 78.55%.
1998–2001	Secondary school from Patna Central School, Patna with aggregate of 91.00%.

## Technical Skills

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Programming	BASIC, C, C++, Java, Visual Basic, Assembly Level Programming, SQL, XML, PHP, Peoplecode.
Scripting	UNIX Shell Scripting, Javascript, CSS.
ERP Softwares	Peoplesoft
Engineering Software Packages	PSpice, Mentor Graphics' IC Studio, Xilinx ISE, Matlab, OrCAD.
Operating Systems	Windows XP, Windows Vista and Linux (Debian, Gentoo, Fedora, Ubuntu).

## Professional Experience

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Since July 2008	Associate Applications Engineer at Oracle India Pvt. Ltd., Bangalore, India. Responsible for design and development of Peoplesoft applications.
May 2007–July 2007	Summer internship at University of Southern California, Los Angeles, California, USA. Developed a non-linear dynamic model of neuron using Bayesian Inference technique in MATLAB environment.
May 2006–June 2006	Summer training at Department of Computer Science and Engineering, IIT Kharagpur. Learnt about Embedded Systems and developed a digital thermometer using Atmel AT89S51 microcontroller.

## Awards & Recognitions

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1. Awarded infoUSA Summer Research Fellowship 2007 by University of Southern California, Los Angeles, California, USA.
2. Secured All India Rank of 401 in IIT-JEE 2004 with a percentile of 99.7%.
3. Secured a State Rank of 16 in AIEEE 2004.
4. Secured highest aggregate in secondary school examination at school.

## Personal Projects

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2008	Design and simulation of Field Oriented Control of 3 Phase Permanent Magnet Synchronous Motor
2008	Design of a Travel Portal using PHP, XML and Google Maps API
2008	Development of Personal Information Manager (PIM) using Java and MySQL
2007	Design and Implementation of 4 bit ALU using Xilinx's Spartan 3 FPGA
2007	Design of FIR filters using Convex Optimization
2006	Color Image Profiling using Fuzzy Logic